

PATENT ABSTRACTS OF JAPAN

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(71)Applicant : NEC CORP

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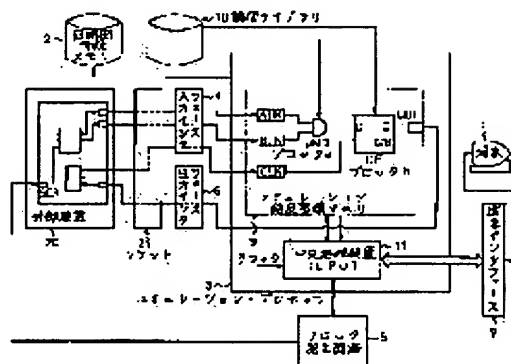
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(54) HARDWARE-EMULATOR

(57)Abstract:

PURPOSE: To execute the evaluation of an apparatus by realizing a pseudo- hardware function of part of an LSI, etc., supplementing an incomplete hardware function, and monitoring an operating state of an internal signal on a terminal.

CONSTITUTION: The hardware emulator comprises input/output interfaces 4 and 6 with an external device 20, a circuit diagram information memory 2 for storing circuit diagram information, an emulation processor 3 for realizing a pseudo-hardware operation according to an input signal and the circuit diagram information, a clock generator 5 for generating a clock to be supplied to the device 20, and a terminal interface 7 having an interface function between an emulation result storage memory 9 for storing an emulation result and a terminal 1. Thus, since the hardware function in an apparatus is emulated according to the circuit diagram information in the memory and a monitor of an internal state can be realized at a terminal, a malfunction of the circuit can be analyzed while operating the apparatus.



LEGAL STATUS

[Date of request for examination]

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24.10.1996

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24.10.2002

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2004/004527

A. CLASSIFICATION OF SUBJECT MATTER
Int.Cl⁷ G01R31/3183

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
Int.Cl⁷ G01R31/28-31/3193, G06F11/22

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Jitsuyo Shinan Koho 1922-1996 Jitsuyo Shinan Toroku Koho 1996-2004
Kokai Jitsuyo Shinan Koho 1971-2004 Toroku Jitsuyo Shinan Koho 1994-2004

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP 6-160482 A (NEC Corp.), 07 June, 1994 (07.06.94), Full text; all drawings (Family: none)	1-17

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

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"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search
01 July, 2004 (01.07.04)

Date of mailing of the international search report
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